

In the Claims:

1. (Currently Amended) A receiving terminal for a CDMA system comprising at least a finger circuit having a plurality of finger circuit elements for ~~taking making~~ a correlation of a received signal from a radio circuit connected to an antenna and a known signal and feeding out the correlated received signal, and a lake circuit for combining a plurality of outputs from the finger ~~circuits~~ circuit elements and executing level measurement, wherein:

the lake circuit includes a level judgment circuit for executing electric field judgment according to the correlated received signal from the finger circuit and a predetermined threshold level, ~~the an~~ operation of ~~a predetermined~~ at least one finger circuit element being suspended for a fixed, predetermined time period according to the result of the level judgment.

2. (Currently Amended) The receiving terminal for a CDMA system according to claim 1, wherein ~~the~~ operation of a control clock supply to the at least one finger circuit is suspended for power consumption reduction according to the result of the level judgment in the level ~~judging~~ judgment circuit.

3. (Currently Amended) The receiving terminal for a CDMA system according to claim 1, wherein ~~the~~ operation of a control clock supply to a timing circuit in the at least one finger circuit element is suspended according to the result of level judgment in the level ~~judging~~ judgment circuit.

4. (Currently Amended) The receiving terminal for a CDMA system according to claim 1, wherein ~~the~~ operation of a control clock supply is suspended after the lapse of a predetermined period of time.

5. (Cancelled)

6. (Currently Amended) The receiving terminal for a CDMA system according to claim 1, wherein the predetermined threshold ~~value~~ level is preset in a memory.

7. (Currently Amended) The receiving terminal for a CDMA system ~~according to 4~~ according to 6, wherein the memory is an E²PROM, and the predetermined threshold data level therefrom is supplied under CPU control to the lake circuit.

8. (Currently Amended) The receiving terminal for a CDMA system according to claim 1, wherein the finger circuit ~~takes~~ makes a correlation of output the received signal data fed out from the radio circuit and data of said known signal ~~data~~ to each other, demodulates the correlated data to symbol unit data, and feeds out the demodulated data to the lake circuit.

9. (Currently Amended) The receiving terminal for a CDMA system according to claim 1, wherein the level measurement is executed by computing the power level in a pilot symbol part in one frame for each slot and adding together the results of the computation for one frame for said plurality of finger circuit elements.

10. (Currently Amended) The receiving terminal for a CDMA system according to claim 1, wherein the ~~finger circuit includes a plurality of finger circuit elements, which each level judgment circuit obtains the difference differences of the~~ a maximum level of electric field levels from among said plurality of finger circuit elements and a pertinent level among the an electric field levels level of each of said plurality of finger circuit elements in them and compare compares the difference differences and with the predetermined threshold value level with each other.

11. (Currently Amended) A receiving terminal for a CDMA system for receiving received signals from a plurality of signal propagation channels, wherein:

the electric field level of the received signal from each signal propagation channel is judged, and ~~the operation of a~~ control clock supply to a circuit system receiving signal from a low electric field level signal propagation channel is suspended for a fixed, predetermined period of time for power consumption reduction.

12. (Currently Amended) A receiver for a CDMA system comprising at least a finger circuit having a plurality of finger circuit elements for taking-making a correlation of a received signal from a radio circuit connected to an antenna and a known signal and feeding out the correlated received signal, and a lake circuit for combining a plurality of outputs from the finger ~~circuits~~ circuit elements and executing level measurement, wherein:

the lake circuit includes a level judgment circuit for executing electric field judgment according to the correlated received signal from the finger circuit and a predetermined threshold level, ~~the an~~ operation of ~~a predetermined~~ at least one finger circuit element being suspended for a fixed, predetermined time period according to the result of the level judgment.

13. (Currently Amended) The receiver for a CDMA system according to claim 12, wherein ~~the operation of a~~ control clock supply to the at least one finger circuit is suspended for power consumption reduction according to the result of the level judgment in the level ~~judging~~ judgment circuit.

All
cont
14. (Currently Amended) The receiver for a CDMA system according to claim 12, wherein ~~the operation of a~~ control clock supply to a timing circuit in the at least one finger circuit element is suspended according to the result of level judgment in the level ~~judging~~ judgment circuit.

15. (Currently Amended) The receiver for a CDMA system according to claim 12, wherein ~~the operation of a~~ control clock supply is suspended after the lapse of a predetermined period of time.

16. (Cancelled)

17. (Currently Amended) The receiver for a CDMA system according to claim 12, wherein the threshold ~~value~~ level is preset in a memory.

18. (Currently Amended) The receiver for a CDMA system according to ~~45~~17, wherein the memory is an E2PROM, and the predetermined threshold data level therefrom is supplied under CPU control to the lake circuit.

19. (Currently Amended) The receiver for a CDMA system according to claim 12, wherein the finger circuit ~~takes~~makes a correlation of ~~output the received signal data fed out~~ from the radio circuit and data of said known signal ~~data~~ to each other, demodulates the correlated data to symbol unit data, and feeds out the demodulated data to the lake circuit.

20. (Currently Amended) The receiver for a CDMA system according to claim 12, wherein the level measurement is executed by computing the power level in a pilot symbol part in one frame for each slot and adding together the results of the computation for one frame for said plurality of finger circuit elements.

All Cont
21. (Currently Amended) The receiver for a CDMA system according to claim 12, wherein the ~~finger circuit includes a plurality of finger circuit elements, which each level judgment circuit~~ obtains the ~~difference differences of the a~~ maximum level of electric field levels from among said plurality of finger circuit elements and a pertinent level among the an electric field ~~levels~~ level of each of said plurality of finger circuit elements in them and ~~compare compares the difference differences and with the predetermined threshold value level with each other.~~

22. (Currently Amended) A receiver for a CDMA system for receiving received signals from a plurality of signal propagation channels, wherein:

~~the an~~ electric field level of the received signal from each signal propagation channel is judged, ~~and the operation of a~~ control clock supply to a circuit system receiving signal from a low electric field level signal propagation channel is suspended for a fixed, predetermined period of time.

23. (Currently Amended) A receiving method for a CDMA system with step for ~~taking~~making a correlation of a received signal and a known signal and combining a plurality of correlated signals for level measurement, the method further comprising:

executing electric field judgment according to the correlated received signal and a predetermined threshold level, and suspending an operation of a predetermined circuit for a fixed, predetermined time period according to the result of the level judgment.

24. (Currently Amended) A receiving method for a CDMA system for receiving received signals from a plurality of signal propagation channels including steps of:

judging ~~the~~an electric field level of the received signal from each signal propagation channel; and

suspending ~~the~~operation of a control clock supply to a circuit receiving signal from a low electric field level signal propagation channel for a fixed, predetermined period of time.